

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	25	((master and slave) same (microcontroller)) same (compar\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/11/05 09:28
L2	118	(lock\$step and microcontroller and emulat\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/11/05 09:30
L3	25	((master and slave) same (microcontroller)) same (compar\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/11/05 09:37
L4	132	(microcontroller same (fpga emulat\$4) same (lock\$step synchroniz \$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/11/05 09:37
L5	42	((processor cpu microprocessor) and (emulator emulation) and (synchronization synchronized lock\$step synchronous)).clm.	US-PGPUB	OR	ON	2008/11/05 09:47
L6	120	((processor cpu microprocessor microcontroller) and (emulator emulation simulator simulation) and (breakpoint halt debug)).clm.	US-PGPUB	OR	ON	2008/11/05 09:50

11/ 5/ 08 10:14:16 AM

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